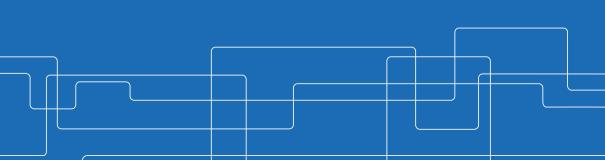


# From core to clusters: tailoring optimization algorithms to modern compute architectures

Z. Wang, J. Lindbäck, V. Mai, J. Zhang, X. Wu, and Mikael Johansson





## A hardware–algorithm disconnect

Modern ML requires solving larger optimization problems, faster:

- training models with billions of parameters using trillions of data tokens
- solving multi-million variable LPs in every training iteration for generative models



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Compute scales and diversifies, but algorithms are often designed with idealized assumptions – sequential execution, uniform memory, perfect communication.

Hardware-algorithm disconnect creates practical paradoxes

- GPU-friendly  $O(n^3)$  algorithms may outperform  $O(n^2)$  methods
- "optimal" distributed algorithms spend 80% of time waiting while cores idle



# Hardware-aware optimization is more than implementation

Hardware-aware optimization is more than just implementation tricks:

- bottlenecks often block the asymptotics from kicking in
- algorithms aligned with the compute fabric are more efficient in practice



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Hardware-aware optimization is more than just implementation tricks:

- bottlenecks often block the asymptotics from kicking in
- algorithms aligned with the compute fabric are more efficient in practice

This requires restructuring the algorithm design itself:

- reorganizing computation patterns for parallel execution (SIMD/SIMT)
- redesigning memory access for coalescence and locality
- overlapping communication with computation

Large gains from rethinking algorithms to respect hardware constraints!



GPU acceleration: optimal transport solver

- Douglas-Rachford method restructured for GPU warp execution patterns
- memory access reorganized for coalescence and bandwidth efficiency
- result: 10–100× speedup over standard implementations



## This talk: two hardware-aware optimization designs

GPU acceleration: optimal transport solver

- Douglas-Rachford method restructured for GPU warp execution patterns
- memory access reorganized for coalescence and bandwidth efficiency
- result: 10–100× speedup over standard implementations

Distributed training: a system-level design

- overlap communication and computation, use bottleneck-aware topology designs
- a decentralized Adam optimizer redesigned for small local batch effects and asynchrony
- result: 30–60% faster wall-clock time + better generalization

Results from concrete algorithm restructuring – not just efficient implementation!



## Contents

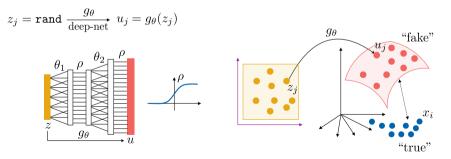
- Motivation
- A GPU-based optimal transport solver
- An efficient decentralized DNN training system
- Conclusions and outlook



## **Optimal transport in machine learning**

Optimal transport (OT): cheapest way to transform one probability distribution into another

- defines a geometrically meaningful distance
- has tons of ML applications: domain adaptation, generative models, learning to rank, ...



OT cost measures distance between distribution of true and generated images.



Given

- two discrete probability distributions  $p \in \mathbf{R}^m$  and  $q \in \mathbf{R}^n$ ,
- a matrix  $C \in \mathbf{R}^{m \times n}$  where  $C_{ij}$  is the cost for moving mass from bin  $p_i \mapsto \text{bin } q_j$ ,

find transportation plan X that solves

minimize 
$$\langle C, X \rangle$$
  
subject to  $X\mathbf{1} = p, \quad X^T\mathbf{1} = q$   
 $X \ge 0$ 

For appropriate C, optimal value is Wasserstein distance:  $W(p, q) = \langle C, X^* \rangle$ 



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For appropriate C, optimal value is Wasserstein distance:  $W(p, q) = \langle C, X^* \rangle$ 

A huge linear program: quickly many millions of variables.

- unless special structure in C, complexity is  $O((m + n)^3)$ .



#### Entropic regularization and the Sinkhorn algorithm

Replace non-negativity with entropic penalty term

$$\begin{aligned} \mathcal{W}_{\varepsilon}(p,q) &= \min_{X} & \langle C,X\rangle - \varepsilon H(X) \\ & \text{subject to} & X\mathbf{1} = p, \quad X^{\mathsf{T}}\mathbf{1} = q \end{aligned}$$

where  $H = -\sum_{i} \sum_{j} X_{ij} \log X_{ij}$  is the entropy of X.



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where  $H = -\sum_{i} \sum_{j} X_{ij} \log X_{ij}$  is the entropy of X.

A convex optimization problem with simple constraints.

An approximation to optimal transport. Can (often) be solved very quickly.



### The Sinkhorn algorithm

The Sinkhorn algorithm, starts with  $v_0 = \mathbf{1}$ , and alternates

$$u_{k+1} = p/Kv_k, \quad v_{k+1} = q/K^T u_{k+1}$$

where division is elementwise and  $K_{ij} = \exp(-C_{ij}/\varepsilon)$ .



#### The Sinkhorn algorithm

The Sinkhorn algorithm, starts with  $v_0 = 1$ , and alternates

$$u_{k+1} = p/Kv_k, \quad v_{k+1} = q/K^T u_{k+1}$$

where division is elementwise and  $K_{ij} = \exp(-C_{ij}/\varepsilon)$ .

Optimal solution to entropy-regularized OT is  $X_{\varepsilon}^{\star} = \operatorname{diag}(u^{\star}) \operatorname{K} \operatorname{diag}(v^{\star})$ .



#### The Sinkhorn algorithm

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where division is elementwise and  $K_{ij} = \exp(-C_{ij}/\varepsilon)$ .

Optimal solution to entropy-regularized OT is  $X_{\varepsilon}^{\star} = \operatorname{diag}(u^{\star}) K \operatorname{diag}(v^{\star})$ .

Widely used, but has many limitations:

- sensitive to  $\varepsilon$ : too small risks numerical instability; too large causes blurry  $X_{\varepsilon}$
- solution (if found) does not define a proper distance
- cannot encourage sparsity or structure...



# Can we design a scalable algorithm for the real OT problem?

Perhaps, if we make clever use of modern hardware, such as GPUs.

GPUs offer massive parallelism, but are memory bound

- Registers: fastest (1 cycle), few KB
- Shared memory: fast (100 cycles), 48–96KB/block
- Global memory: large but slow (500+ cycles)

Key to success: algorithm should fit in fast memory, minimize memory traffic, avoid branching

Our approach: Douglas-Rachford splitting



#### **Douglas-Rachford splitting**

Solves problems on the form

$$\min_{x} \inf f(x) + g(x)$$

where f and g are closed convex functions, with cheap prox-operators.

For penalty parameter  $\rho > 0$ , repeat

$$x^{k+1} = \operatorname{prox}_{\rho f}(y^{k})$$
  
$$y^{k+1} = y^{k} + \operatorname{prox}_{\rho g}(2x^{k+1} - y^{k}) - x^{k+1}$$

Global convergence of  $\{x_k\}$  to x such that  $0 \in \partial f(x) + \partial g(x)$ , for all values of  $\rho > 0$ .



#### How to split the optimal transport?

A result by Bauschke et al. (2021) gives a hint.

**Lemma.** Let  $p, q \ge 0$  satisfy  $\mathbf{1}^T p = \mathbf{1}^T q$ . Then,

$$\mathcal{X} = \left\{ X \in \mathbf{R}^{m \times n} \mid X \mathbf{1} = p, \quad X^T \mathbf{1} = q \right\}$$

is non-empty, and for every given  $X \in \mathbb{R}^{m \times n}$ , we have

$$\mathsf{P}_{\mathcal{X}}(X) = X - \frac{1}{n} \left( (X\mathbf{1} - p)\mathbf{1}^{\mathsf{T}} - \gamma \mathbf{1}\mathbf{1}^{\mathsf{T}} \right) - \frac{1}{m} \left( \mathbf{1} (X^{\mathsf{T}}\mathbf{1} - q)^{\mathsf{T}} - \gamma \mathbf{1}\mathbf{1}^{\mathsf{T}} \right) := X + \phi \mathbf{1}^{\mathsf{T}} + \mathbf{1}\varphi^{\mathsf{T}}$$

where  $\gamma = \mathbf{1}^{T} (X\mathbf{1} - p) / (m + n) = \mathbf{1}^{T} (X^{T}\mathbf{1} - q) / (m + n).$ 

Projection computed using only matrix-vector multiplies/rank-one updates.



# **DROT: DR splitting for OT**

Re-writing the OT problem as

$$\underset{X}{\text{minimize}} \underbrace{\langle C, X \rangle + \iota_{\mathsf{R}^{m \times n}_{+}}(X)}_{f(X)} + \underbrace{\iota_{\mathcal{X}}(X)}_{g(X)}$$

yields the DR iterations

$$X^{k+1} = [Y^k - \rho C]_+$$
  
$$Y^{k+1} = X^{k+1} + \phi_{k+1} \mathbf{1}^\top + \mathbf{1} \varphi_{k+1}^\top$$

Eliminating Y, we only need to manipulate a single large matrix

$$X^{k+1} = [X^k + \phi_k \mathbf{1}^\top + \mathbf{1}\varphi_k^\top - \rho C]_+$$

No exponentials, no divisions, only rank-one updates.



Organize GPU threads into a 2D grid of blocks

- each thread block (SP) handles a submatrix of X
- each thread handles a single column (for memory efficiency)



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Respect the memory hierarchy

- registers for private data, shared memory for common data across threads (e.g.  $\phi$ ,  $\varphi$ )
- perform partial reductions (e.g. row sums, cost evaluation) during matrix updates
- fuse operations into a single kernel pass to minimize memory transactions



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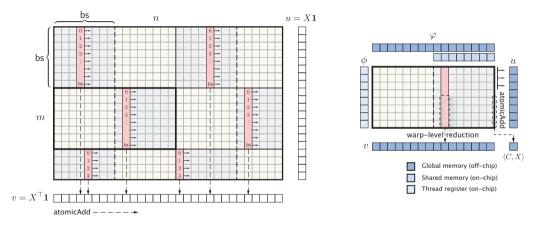
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- fuse operations into a single kernel pass to minimize memory transactions

Reduce global memory access

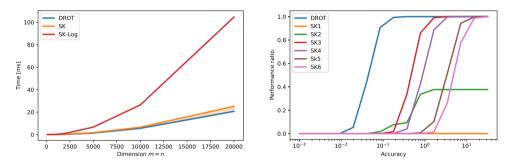
- each element of X is read or written once per iteration
- can skip loading C every other iteration (by writing  $X^+ \rho C$ )







#### **Numerical results**



Faster and more robust than Sinkhorn. Solves the true OT problem, returns sparse plans.



# Adding regularization to DROT

Regularization: standard approach for encouraging structure in solution

minimize  $\langle C, X \rangle + h(X)$ subject to  $X\mathbf{1} = p, \quad X^{T}\mathbf{1} = q$  $X \ge 0$ 

Useful in practice, but non-trivial for Sinkhorn.



# Adding regularization to DROT

Regularization: standard approach for encouraging structure in solution

minimize  $\langle C, X \rangle + h(X)$ subject to  $X\mathbf{1} = p, \quad X^T\mathbf{1} = q$  $X \ge 0$ 

Useful in practice, but non-trivial for Sinkhorn.

We can extend the DROT approach to a family of useful regularizers.

**Definition.** *h* is sparsity promoting if  $h(\tilde{X}) \leq h(X)$  for every  $\tilde{X}$  with  $\tilde{X}_{ij} \in \{0, X_{ij}\}$ 

Includes weighted  $\ell_1$ , group-lasso, quadratic, constrained OT, and more ...



#### Sparsity-promoting regularizers

**Proposition.** Let  $f(X) = \langle C, X \rangle + \mathbf{I}_{\mathbb{R}^{m \times n}}(X) + h(X)$  with *h* sparsity promoting. Then

 $\operatorname{prox}_{\rho f}(X) = \operatorname{prox}_{\rho h}([X - \rho C]_+)$ 

**Consequence.** splitting as

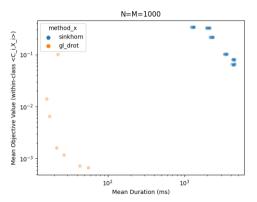
$$\underset{X}{\text{minimize}} \underbrace{\langle C, X \rangle + h(X) + \iota_{\mathsf{R}^{m \times n}_{+}}(X)}_{f(X)} + \underbrace{\iota_{\mathcal{X}}(X)}_{g(X)}$$

results in a minimal update of the DROT approach. Efficient when prox is GPU-compatible.



# Numerical results: domain adaptation using group-lasso regularized OT

Comparison with method of Courty et al. (sequential linearization, solved by SK)



Speedups of over 100x, with better accuracy!



#### **Theoretical guarantees**

**Theorem. (informal)** Let  $\{X_k\}$  be generated by DROT. Then

$$\langle C, \bar{X}_k \rangle + h(\bar{X}_k) - (\langle C, X^* \rangle + h(X^*)) \leq \frac{c}{k}$$

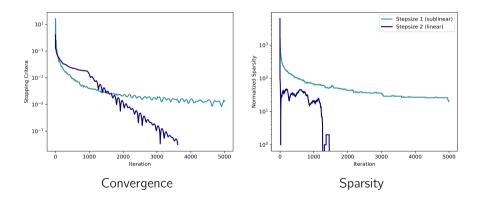
where  $\bar{X}_k = \sum_{i=1}^k X_k/k$  and c is a constant depending on  $X_0, Y_0, X^*$  and  $Y^*$ .

Significant improvement over SK's  $\mathcal{O}(1/\sqrt{k})$ , but not how method actually behaves.



# Convergence behaviour in practice

Initial 1/k rate followed by fast linear convergence when sparsity stabilizes.





#### Takeaways

DROT: GPU-native optimal transport solver

- accurate: solves true OT problem, not an approximation
- flexible: easily incorporates sparsity-promoting regularizers
- fast: up to 100x speedup vs Sinkhorn; avoids exponentials, divisions

Hardware-awareness is key: from a method that "does not work" to one that excels!



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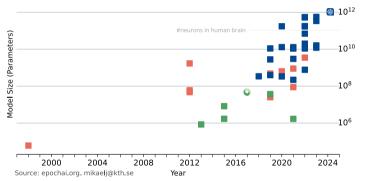
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#### The challenge of large-scale model training

#### An explosion in model size

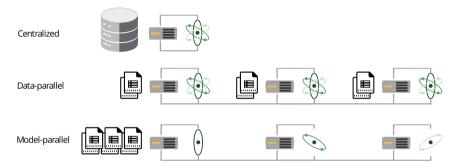
Number of parameters in notable AI models



Implies similar explision in training data (Chinchilla scaling: 20 tokens/parameter)



#### Parallelizing large-scale training



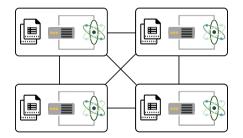
Two main approaches

- Distribute data: data parallelism (our focus)
- Distribute compute: model, pipeline, and tensor parallelism



# Decentralized optimization for large-scale training

Q: Can we make decentralized optimization competitive for large-scale DNN training?



Earlier attempts fail to beat synchronous training on wall-clock time/validation loss.

Our recipe: eliminate all idle times, resist stragglers, respect heterogeneity.

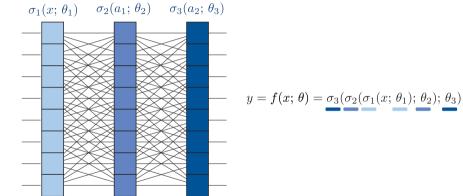


# Contents

- Motivation
- Basics of DNN training
- Our proposal
- Conclusion



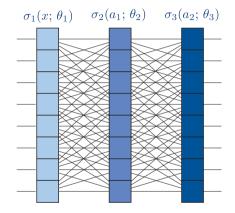
## **Basics of DNN training**



x



## **Basics of DNN training**



Forward pass: evaluate f (and activations)

$$a_{0} = x$$

$$a_{1} = \sigma_{1}(a_{0}; \theta_{1})$$

$$a_{2} = \sigma_{2}(a_{1}; \theta_{2})$$

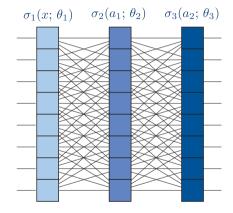
$$a_{3} = \sigma_{3}(a_{2}; \theta_{3})$$

$$y = a_{3}$$

x



## **Basics of DNN training**



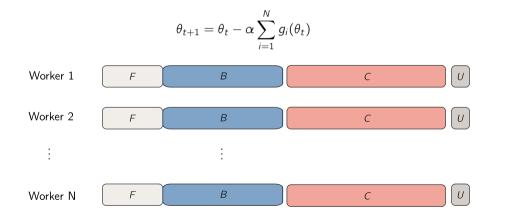
Backward pass: compute gradients

$$\begin{array}{c} \frac{\partial \ell(y)}{\partial \theta_3} = \frac{\partial \ell}{\partial y} \cdot \frac{\partial y}{\partial \theta_3} = \frac{\partial \ell}{\partial y} \cdot \frac{\partial \sigma_3}{\partial \theta_3}(a_2) \\ \\ \frac{\partial \ell(y)}{\partial \theta_2} = \frac{\partial \ell}{\partial y} \cdot \frac{\partial \sigma_3}{\partial a_2} \cdot \frac{\partial \sigma_2}{\partial \theta_2}(a_1) \\ \\ \frac{\partial \ell(y)}{\partial \theta_1} = \frac{\partial \ell}{\partial y} \cdot \frac{\partial \sigma_3}{\partial a_2} \cdot \frac{\partial \sigma_2}{\partial a_1} \cdot \frac{\partial \sigma_1}{\partial \theta_1}(x) \end{array}$$

x



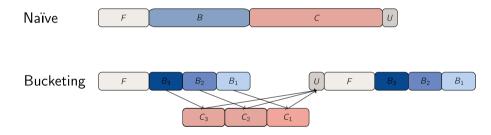
## DNN training using parallel gradient descent





## **Reducing idle time**

Bucketing: communicate gradient blocks (layers) when they are done

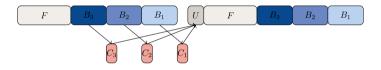


Simple idea, but can yield substantial improvements!



Why AllReduce can be hard to beat...

Parallel GD hard to beat with fast communication, homogeneous hardware, workloads.

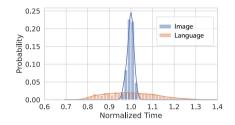


Can happen with few workers, or in the very high-end of dedicated HPC clusters

Still need to wait until all gradients are computed and communicated.



# ... and why decentralized optimization could be superior



Natural language tasks introduce work-load variations.

Many HPC clusters have heterogeneous hardware, limited interconnect

Global synchronization (waiting for slowest node) becomes increasingly wasteful!



## Our proposal

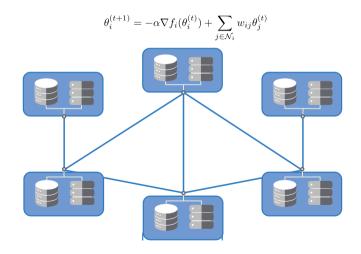
A framework for data-parallel training of DNNs based on:

- a decentralized system with GPUs as workers
- a decentralized Adam algorithm, with a twist
- use of time-varying (switched) communication topology to deal with heterogeneity
- an execution model for efficient system tuning

Resilient to workload variations, system noise and hardware heterogeneity.



## A few words about decentralized optimization





A decentralized Adam algorithm based on decentralized gradient descent

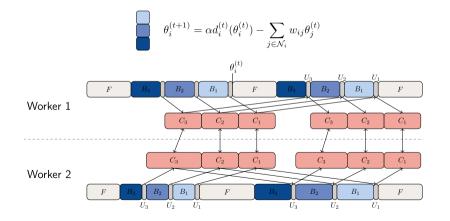
$$\theta_i^{(t+1)} = \alpha d_i^{(t)}(\theta_i^{(t)}) + \sum_{j \in \mathcal{N}_i} w_{ij} \theta_j^{(t)}$$

Rationale:

- Sharing models instead of gradients allows to increase overlap dramatically
- Nodes only share models with neighbors (in a communication topology)
- Adam essential for training transformer-based architectures



Resilient to stragglers, can approach 100% utilization





Algorithm 1 Decentralized Adam on worker *i* 

$$\begin{split} m_i^{(0)}, v_i^{(0)} &\leftarrow 0; \ \theta_i^{(0)} \leftarrow \theta^{(0)} \\ \text{for } t = 1, 2, \dots, T \text{ do} \\ g_i^{(t)} &\leftarrow \nabla \ell(\theta_i^{(t)}; \xi_i^{(t)}) \\ m_i^{(t)} &\leftarrow \beta_1 m_i^{(t-1)} + (1 - \beta_1) g_i^{(t)} \\ v_i^{(t)} &\leftarrow \beta_2 v_i^{(t-1)} + (1 - \beta_2) [g_i^{(t)}]^2 \\ \theta_i^{(t+1)} &\leftarrow -\alpha \frac{m_i^{(t)}/(1 - \beta_1^t)}{\sqrt{v_i^{(t)}/(1 - \beta_2^t) + \epsilon}} + \sum_{j \in \mathcal{N}_i} w_{ij} \theta_j^{(t)} \\ \text{end for} \end{split}$$



**Theorem.** If Algorithm 1 uses  $0 < \beta_1 < \beta_2 < 1$ , then

$$\mathbf{E}\left[\|\nabla \ell(\bar{\theta}^{(\tau)})\|_{2}^{2}\right] \leq \frac{4R}{\alpha \tilde{T}} \left(\ell(\bar{\theta}^{(0)}) - \ell^{*}\right) + E\left[\frac{1}{\tilde{T}}\ln\left(1 + \frac{R}{\epsilon(1-\beta_{2})}\right) - \frac{T}{\tilde{T}}\ln(\beta_{2})\right], \quad (1)$$

where  $\bar{\theta}^{(t)} = \frac{1}{N} \sum_{i=1}^{N} \theta_i^{(t)}$ ,  $\tau$  is a random stopping time, and  $E \sim \alpha^2$ .

Note. Last term new compared to single-machine setting, but vanishes quickly with  $\alpha$ .



## The vanishing mini-batch problem

For good generalization performance, the batch size should not be too large.

- with fixed batch-size, local mini-batches vanish in size as worker count increases
- small batches gives high-variance of momentum parameter updates



#### The vanishing mini-batch problem

For good generalization performance, the batch size should not be too large.

- with fixed batch-size, local mini-batches vanish in size as worker count increases
- small batches gives high-variance of momentum parameter updates

Proposed fix: accumulate gradients, update momentum parameters every *s* iterations.

 $\Rightarrow$ 

$$g_{i}^{(t)} \leftarrow \nabla \ell(\theta_{i}^{(t)};\xi_{i}^{(t)}) \\ m_{i}^{(t)} \leftarrow \beta_{1}m_{i}^{(t-1)} + (1-\beta_{1})g_{i}^{(t)} \\ v_{i}^{(t)} \leftarrow \beta_{2}v_{i}^{(t-1)} + (1-\beta_{2})[g_{i}^{(t)}]^{2}$$

$$m_{i}^{(t)} \leftarrow \beta_{1} \bar{m}_{i}^{(t-1)} + (1 - \beta_{1}) g_{i}^{(t)}$$

$$v_{i}^{(t)} \leftarrow \beta_{2} \bar{v}_{i}^{(t-1)} + (1 - \beta_{2}) [g_{i}^{(t)}]^{2}$$

$$b_{i}^{(t+1)} \leftarrow b_{i}^{(t)} + g_{i}^{(t)} / s$$
**if**  $t \mod s == 0$  **then**  
update  $\bar{m}_{i}, \bar{v}_{i}$  based on  $b_{i}$   
**end if**

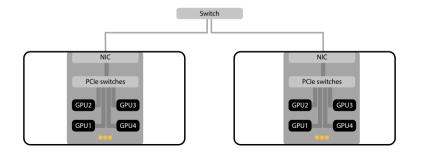


# Communication in a typical HPC cluster

Simple graph abstraction of communication topology is not enough.

- intra-node communication (bus) is much faster (say, 20x) than inter-node
- congestion can occur at switches and interfaces

Need to respect this when designing communication pattern (and mixing weights  $w_{ij}$ )!



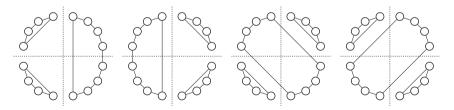


## What is the optimal communication scheme?

We do not know, but clearly

- limiting inter-node communication reduces costly communications and congestion
- increasing intra-node communication enhances convergence

We propose to use an alternating exponential ring:

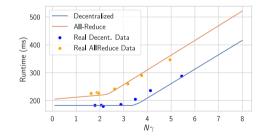




#### An execution model

Cannot hope to have better iteration-complexity than centralized optimization

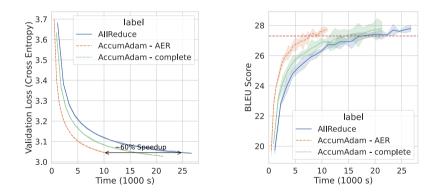
- should try to optimize per-iteration runtime
- captured by run-time model (communication/compute times, workload variations)





#### **Evaluation**

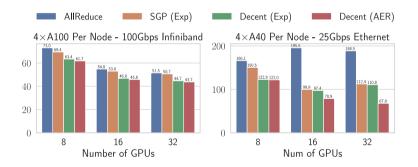
Training of transformer for English-to-German translation task (65M parameters)





#### **Evaluation**

Scaling in high-performance and communication-bound scenarios (GPT-2 training)





## Takaways

Decentralized optimization for large-DNN training

- clearly useful, and perhaps more so in the years to come

Our solution:

- a decentralized system with GPUs as workers
- a decentralized Adam algorithm, with a twist
- use of time-varying (switched) communication topology to deal with heterogeneity
- an execution model for efficient system tuning

Strong practical performance, but many open theoretical problems



# Summary and outlook

Bridging the hardware–algorithm disconnect:

- modern architectures require algorithms designed with hardware in mind.
- hardware-aware design as a core algorithmic principle?



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- modern architectures require algorithms designed with hardware in mind.
- hardware-aware design as a core algorithmic principle?

Two case studies, one message:

- a GPU-native OT solver designed for memory-aware computation, streamlined updates.
- decentralized training resilient to stragglers, hiding communication, eliminating idling



# Summary and outlook

Bridging the hardware–algorithm disconnect:

- modern architectures require algorithms designed with hardware in mind.
- hardware-aware design as a core algorithmic principle?

Two case studies, one message:

- a GPU-native OT solver designed for memory-aware computation, streamlined updates.
- decentralized training resilient to stragglers, hiding communication, eliminating idling

Looking ahead:

- generalize the co-design principles to other classes of optimization problems
- explore tighter theoretical bounds for decentralized and DR-based algorithms
- investigate adaptability: can algorithms dynamically tune to hardware in real-time?